#### UTILIZING A BIG.LITTLE™ SOLUTION IN AUTOMOTIVE

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■ Requirement for big.LITTLE<sup>™</sup> feature on Automotive Market

- Solutions with the Upstream Linux<sup>®</sup> Kernel
- More Performance than battery life
- Parts of EAS contribution from Arm are got accepted by Linux Kernel Community
- Tradeoffs between power consumption and performance
- Solution with the existing upstream features for big.LITTLE
  - How to put tasks to "big" side as possible
- Now it is contributing big.LITTLE feature to AGL UCB<sup>TM</sup>

# Who am I ?

- Name : Yoshiyuki Ito
  - I'm working in Renesas Electronics Corporation., Tokyo, Japan.
- Career:
  - ✓ 1993 2003 : Working on multi-core processor development project in NEC.
  - ✓ 2003 2013 : In charge of Linux<sup>®</sup> for mobile phone in the NEC Electronics and Renesas Electronics.
  - ✓ 2013 : Joined Linux<sup>®</sup> for automotive development project.

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## **Requirement: Solutions with LTS/Upstream Kernel**

Linux kernels of In-vehicle infotainment system (IVI)

-Android Automotive: Android Common Kernel

-AGL and any other IVI than Android: LTS Kernel



→Some part of EAS still integrated only for Android Common Kernel

Hove to be solved how to provide big.LITTLE feature for non-Android IVI



## **Requirement: Performance than Battery Life**

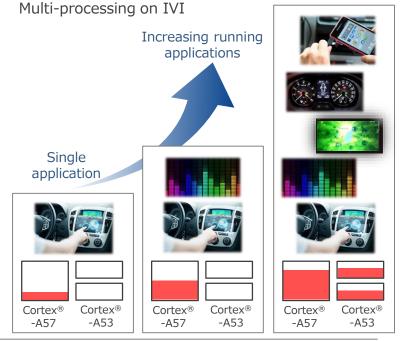
#### Target of original EAS

- −Enhancing BATTERY LIFE ← Reducing Power Consumption
  - And then preparing for heavy tasks To provide better user experience

#### Requirement of Automotive Market

- -Keep the PERFORMANCE even in heavy use case
- ←Stable display framerate and/or response time

are required



**BIG IDEAS FOR EVERY SPACE** 

**I** 

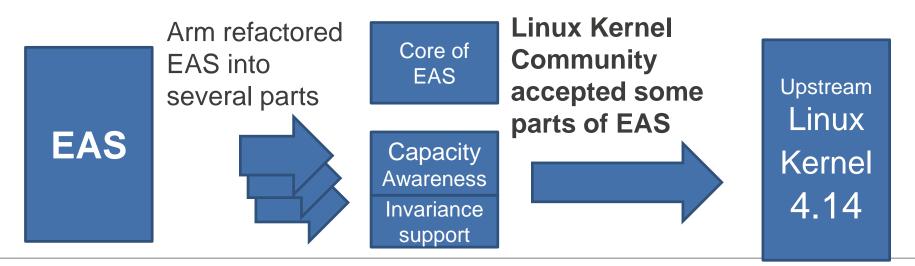
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#### **EAS contribution and Linux Kernel community**

- It got accepted by upstream as some parts of EAS such as capacity awareness, Freq./Arch. invariance support, etc., as a result of Arm contribution effort.
  - Arm refactored EAS into several parts of scheduler functions to contribute for upstream.
  - Core part of EAS is still struggling to contribution (as of 4.14).



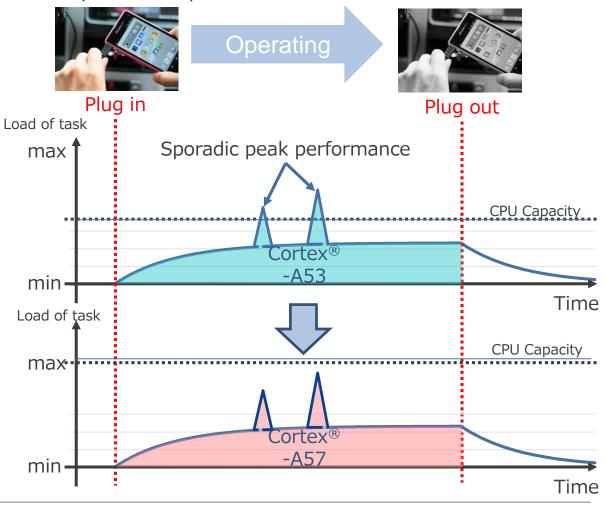


#### Tradeoffs between power consumption and performance

- When put the priority for effectiveness of "BATTELY LIFE"
  - Unexpected task allocation to the LITTLE would be expected
  - Even while it able to allocate big CPU
- → Performance Instability on the "Sporadic peak performance" type of the task

#### Lower response of application manipulation

Connectivity for smart-phones



**BIG IDEAS FOR EVERY SPACE** 

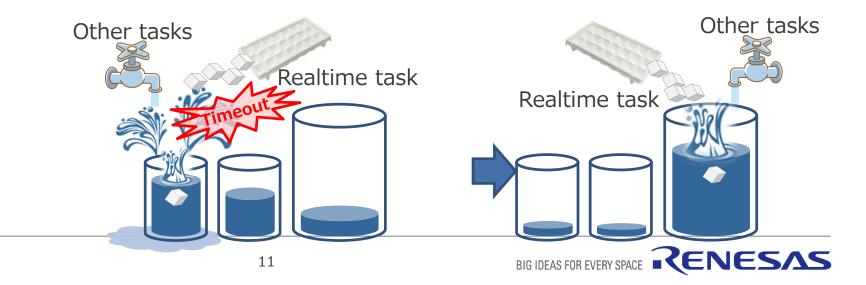
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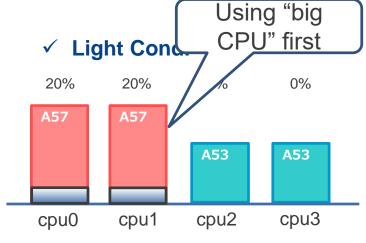
#### How to solve "unexpected task allocation to the LITTLE"

- In the case of Android Common Kernel:
  - It's able to control task assignment via sched tune facilities.
- In the case of Upstream Kernel:
  - It have to be apply parameter tunings to **utilize "big" side** as maximum.

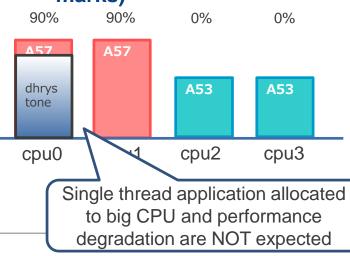


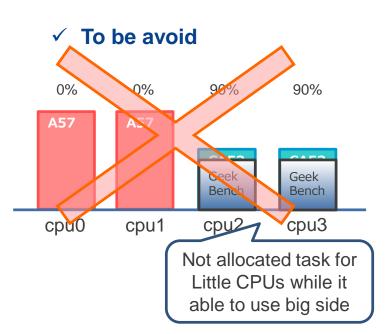
# Parameters to achieve task allocation to utilize "big" side (1/2)

- It must allocate tasks for "big" side
- Preventing degradation of the performance of single thread process
  - Untightening request for power consumption, but put on weight for performance
- Renesas got many suggestions from Arm SW Team to implement how it modify to allocation priority



Single Thread Task (bench marks)

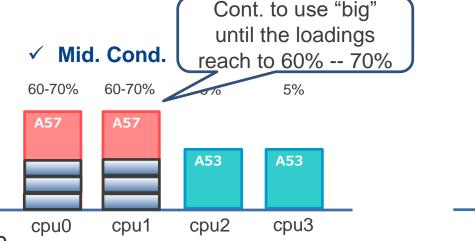


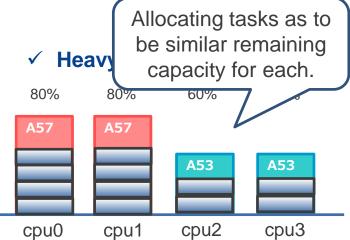


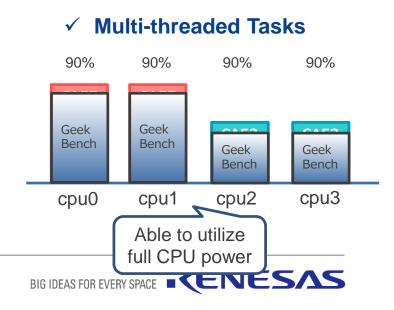
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# Parameters to achieve task allocation to utilize "big" side (2/2)

- When filled out so far the capacity of big side, it starting allocation to the LITTLE side.
  - → It's better to run on LITTLE \_ side instead of the waiting on the run queue.
- It able to achieve that to control task allocation along with making remaining capacity as a similar level on both big side and LITTLE side.
- Renesas consider that it is appropriate to control load level as to corresponds to 60% - 70% on the big side. (Note: It depends on max CPU capacity.)





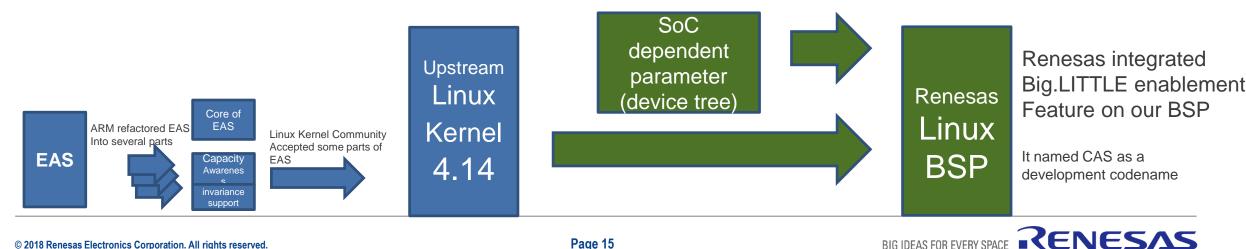


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#### How to utilize big.LITTLE w/ upstream kernel (1/2)

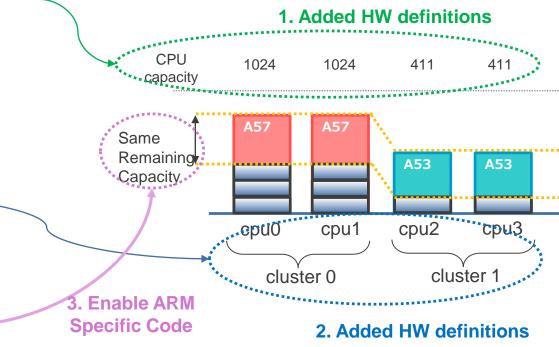
- Already enough when it's not required strict battery life enhancement
  - Renesas: Confirmed those current partial EAS features of the upstream Linux **Kernel** (capacity awareness, Freq./Arch. invariance support, etc.) are enough to utilize big.LITTLE feature for automotive use case.
    - With some additional scheduler parameter refinement via device tree to make CPU performance as optimal.



# How to utilize big.LITTLE w/ upstream kernel (2/2)

#### Just 5 patches required

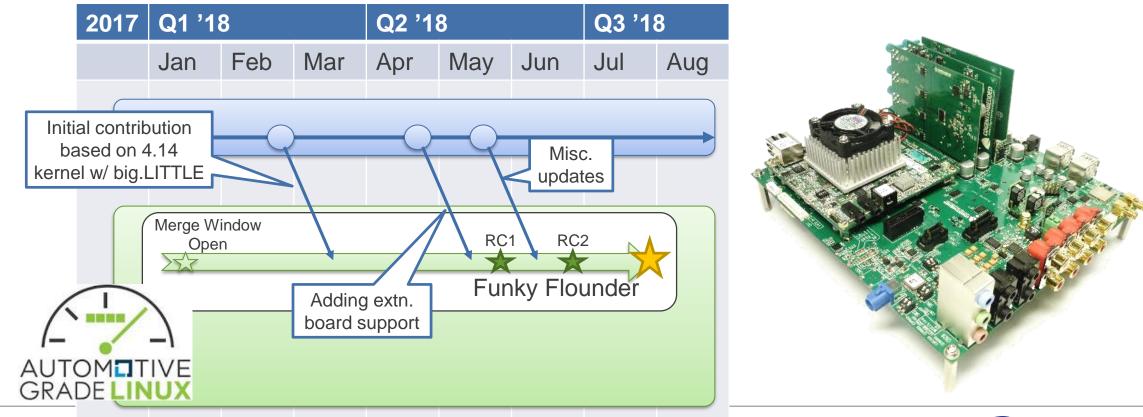
- 1. Added CPU capacity definitions of Cortex®-A57/Cortex®-A53 into the device tree
  - arm64: dts: renesas: r8a7795: Add cpu capacity-dmips-mhz
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- 2. Defines cluster structure of A57/A53 into the device tree
  - arm64: dts: renesas: r8a7795: Add multi-cluster definition
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- 3. Enable the "flag" to capacity awareness of the scheduler
  - soc: renesas: rcar-topology: Add support to be aware cpu capacity



## It's already starting contribution to AGL FF

■Renesas is now contributing BSP with big.LITTLE feature into AGL UCB<sup>™</sup>.

-Also contributing Renesas's small patches to the upstream.



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#### CONCLUSION

- Renesas is now contributing big.LITTLE feature for AGL FF release
  - Which big.LITTLE feature tweaked by parameter setting:
    - To utilizing "big" core as maximum to fit with Automotive use case
- It implemented with small patches for device tree and upstream scheduler
- As a result, it got explicit performance improvement on multi-threaded benchmarks without any performance degrade of single-thread benchmark compared with SMP of "big" cores

And, I'm very appreciate to the Arm Software Team who put heavy effort to contributing EAS to the upstream and held quite fruitful discussion with us about the these patch.



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